SME'S Mahavir Polytechnic, Nashik.

Department: Computer Engineering

Faculty Profile

Ms.Swamini B. Guldagad



Qualification:	BE		
Designation :	Lecturer		
Date of Joining:	06/03/2023		
Experience :	1 Year		
Department:	Computer Engineering		
Courses / Subjects Involved With:	 Microprocessor Computer Peripheral and Hardware Digital Technique Basic Electrical and Electronics Engineering Fundamentals of ICT 		
Staff training/FDP attended in last 3years	 Python Programming and its applications. Advanced Electronics Instruments used by ARM forces 		
Paper published in journals	1) Mugshot Master [Criminal Face Identification]		
Participation in MSBTE activities (RAC/DC/Theory/PR Exam/Curriculum development) List of Projects guided at diploma level last 3 years	RAC- Quality Checker, Examiner MSBTE Exam- Supervisor, External Practical Examiner, Internal Vigilance, Reliever Mugshot Master [Criminal Face Identification]		
Role and responsibilities at institute level			
Role and responsibilities at department level	 Lecturer Class Coordinator LC Coordinator Implant Training Coordinator NBA Criteria Coordinator 		
Additional information	E-Mail Id: swamini.guldaga@shreemahavir.org Blog Link: https://swaminiguldagad.blogspot.com/		